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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:

Aronson et al.

Confirmation No.:

6191

Serial No.:

09/777,917

Art Unit:

2633

Filed:

February 5, 2001

Examiner: Leung, Christina

For:

Integrated Memory Mapped

Attorney Docket No.:

60900-0052-US

Controller Circuit for Fiber Optics Transceiver

INFORMATION DISCLOSURE STATEMENT

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure provisions of 37 C.F.R. §1.56, there is hereby provided certain information which the Examiner may consider material to the examination of the subject U.S. patent application. It is requested that the Examiner make this information of record if it is deemed material to the examination of the application.

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Respectfully submitted,

Date: October 20, 2004

45,645

(Reg. No.)

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CITATION				Filing Date	February 5, 2001
				First Named Inventor	Aronson
PTO-1449				Art Unit	6191
				Examiner Name	Leung, Christina
Sheet	1	of	1	Attorney Docket Number	60900-0052-US

			U.S. PATEN	T DOCUMENTS					
Examiner	Cite No.	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Class	Subclass	Filing Date if Appropriate		
Initials		Number - Kind Code ¹	MM-DD-YYYY						
		5,594,748	01/14/1997	Jabr					
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			FOREIGN PAT	ENT DOCUMENTS					
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Examiner Initials		Document Country Code ² - Number ³ - Kind Code ⁴ (if known)					Yes	No	
·		EP Search Report EP 02 70 4344	10/05/2004	Finisar Corporation					
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